

[21] Pavithra Roy P., and Muralidhar M., "Hidden Markov Model based Channel state prediction in Cognitive Radio Networks" International Journal of Engineering Research & Technology, pp. 391-394, Vol. 4, Issue 2, 2015.

[22] Pavithra Roy P., and Muralidhar M., "Throughput Maximization in Cognitive Radio Networks using Levenberg-Marquardt Algorithm", International Journal of Advanced Research in Computer and Communication Engineering, pp. 315-319, Vol. 4, Issue 2, 2015.

[23] Pavithra Roy P and Muralidhar M., "Channel State Prediction in a Cognitive Radio Network using Neural Network Levenberg-Marquardt Algorithm" International Journal of Wireless Communications and Network Technologies, pp. 24-29, 4(2), 2015.

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